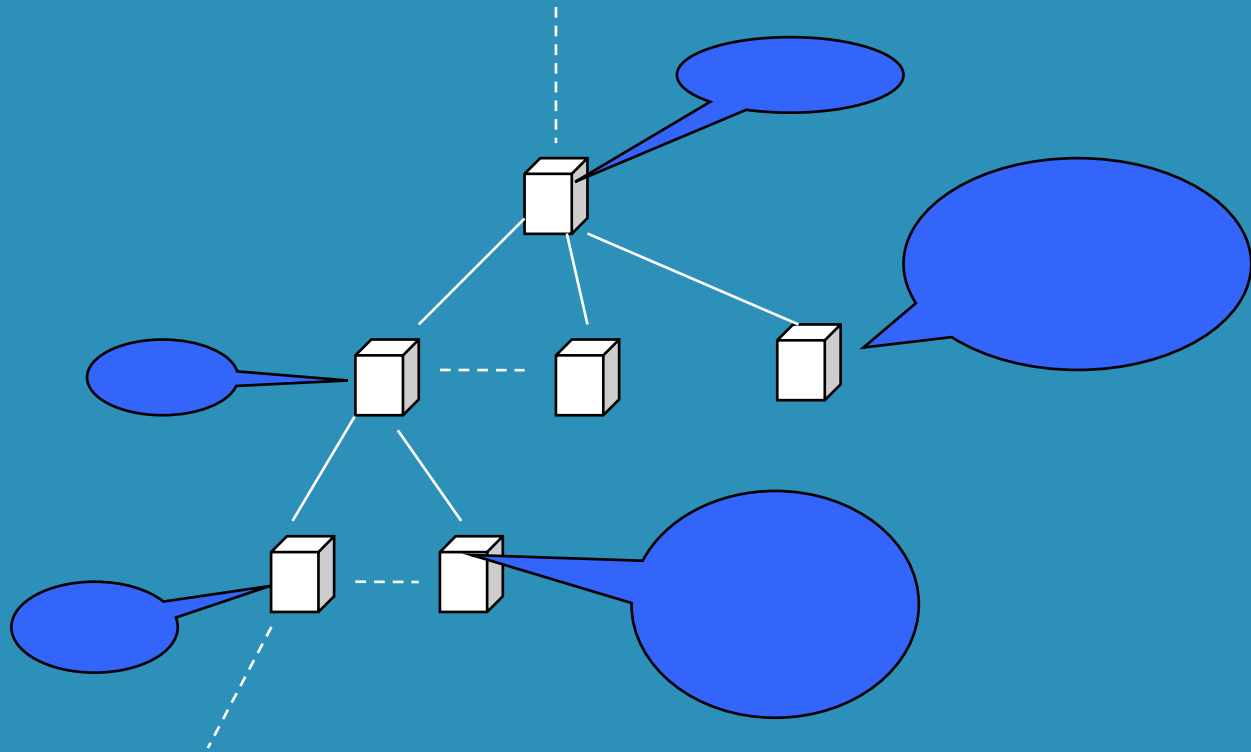


Hierarchical graph



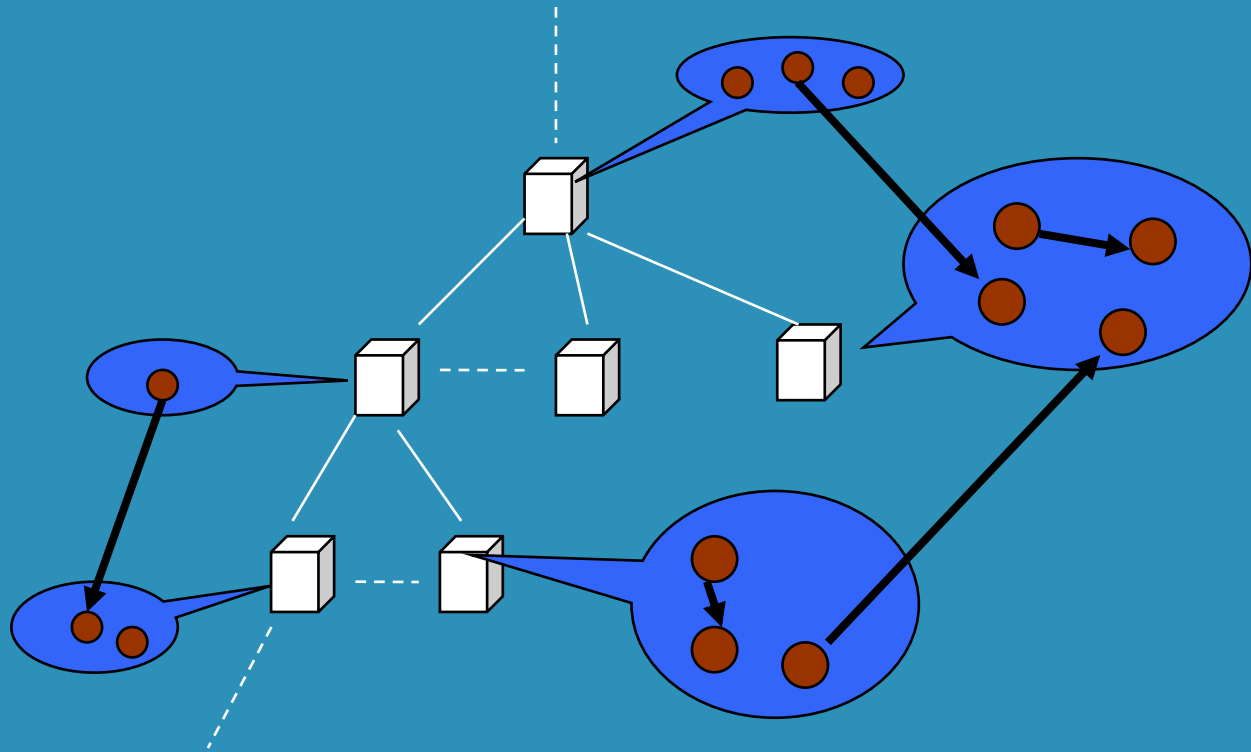
Hierarchy of clocks



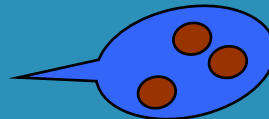
A sub-graph to each clock



Hierarchical graph



- Sub-graph : a set of nodes



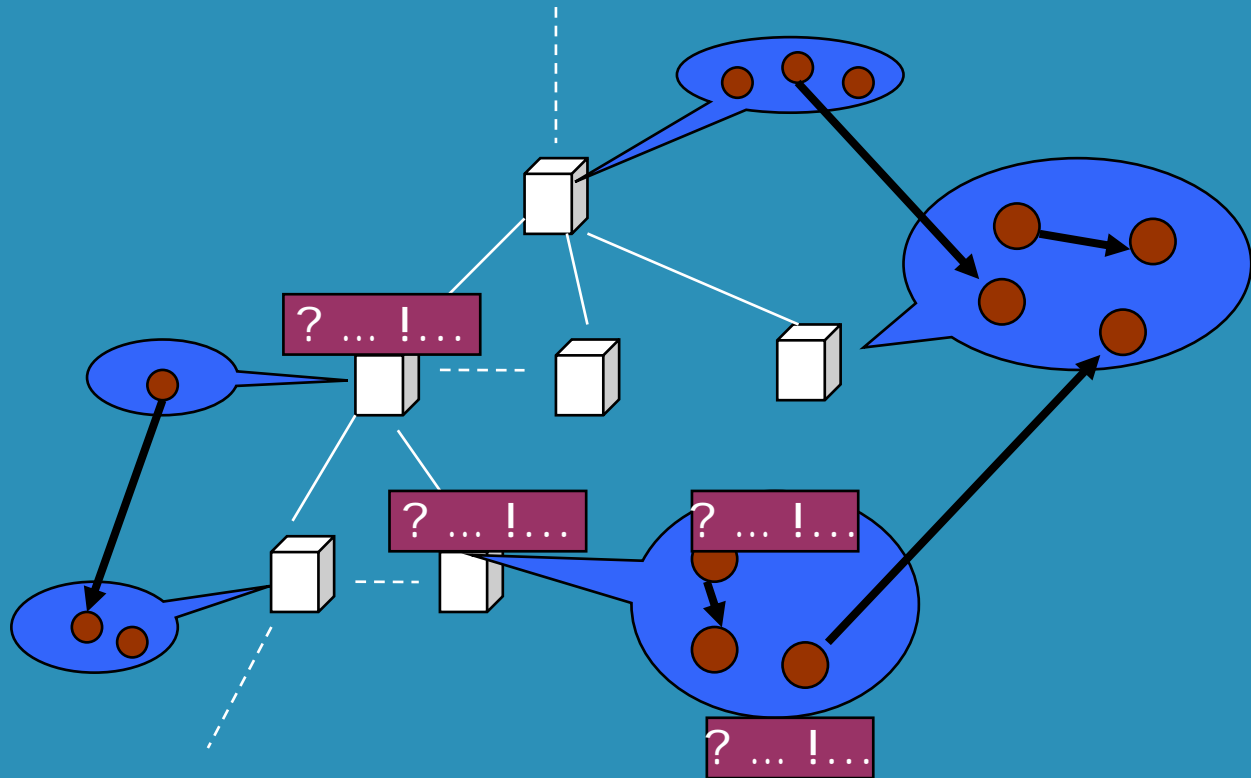
- Nodes : Equation, Memorization, ..., Array



- Dependences



Hierarchical graph



To each structure : an interface (parameters, inputs, outputs)

? ... !...

Graph Travellings

- Travelling the hierarchy : (up-down left right)
 - Signal Source restitution
- Travelling taking account the dependences
 - Code Generators